

BT-EPR-bb series 250W / 500W / 1kW



The BT-EPR-bb is a range of class AB pulsed RF power amplifiers offering ultra fast pulse rise and fall times. They are available in a range of bands from 100MHz to 1GHz.

- Rugged, solid-state design - high reliability
- Extremely high phase and amplitude stability
- Ultra fast pulse rise/fall times
- High linearity
- Very low interpulse noise

Suitable for EPR, ESR, ENDOR and pulsed radar systems which use very short pulses.

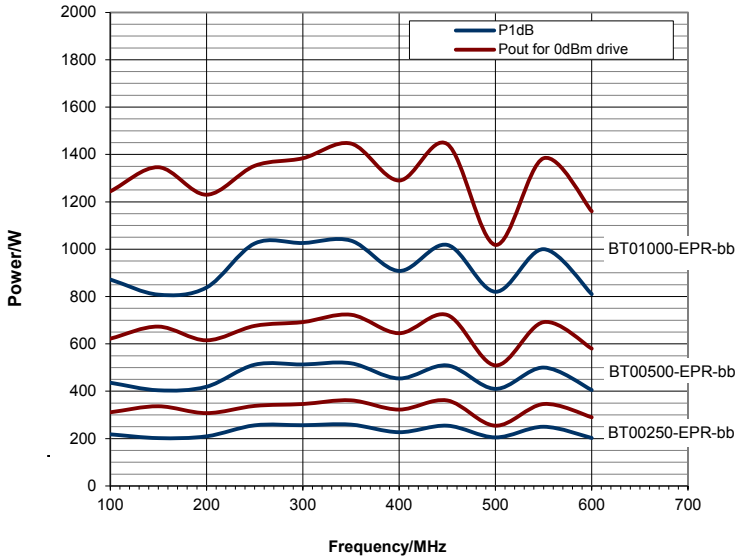
BT-EPR-bb Series

Model numbers	BT0250-EPR-bb / BT0500-EPR-bb / BT1000-EPR-bb
Rated power	250W / 500W / 1kW minimum ¹
P1dB	200W / 400W / 800W minimum ²
Type	Class AB MOSFET
Frequency	100-600MHz 200-750MHz 350-800MHz
Gain flatness	±1.5dB maximum
Max. duty cycle	5% ³
Max. pulse width	10µs ⁴
Pulse droop	0.5dB maximum ⁵
Pulse rise and fall times	<20ns typical from 10% to 90% full power using a pre-gated RF input signal
Gate delay	Rising edge: 50ns typical Falling edge: 50ns typical ⁶
Harmonics	Odd: -16dBc typical, -10dBc maximum Even: -30dBc typical, -20dBc maximum
Spurious	<-70dBc maximum
Output noise (blanked)	<10dB above thermal
Phase change/power	<5° from -40dB to full power
Phase stability	<1° across 10µs pulse
Output sample	-50dB into 50Ω (forward voltage sample)
Input/output impedance	50Ω nominal
Load VSWR	Tolerates at least 3:1 @ full rated power without shut down ⁷
Remote interface	Parallel status monitoring via 25 pin D connector ⁸
Connectors	RF output: N-type RF input, gate, sample: BNC ⁹
Cooling	Forced air
Indicators	DC Power, RF Power, Enable, Selected, Over-temp, Over-duty, Mismatch, Shutdown
Gain control range	10dB minimum for 0-5V control voltage
RF drive RF gate (blanking)	0dBm nominal, 10dBm for no damage 0-5V CMOS
Physical	19" W x 500mmD x 133mmH (3RU x 19" rack mounting) 15kg / 18kg / 22kg
Mains power	110-240V, 50-60Hz, single phase, 250VA max ¹⁰
Typical mains power consumption	65W / 110W / 190W when running at full power, full duty cycle 30W / 40W / 50W standby

1. PEP for input power of 1mW
2. Minimum output power at 1dB gain compression
3. Duty cycle is internally limited in pulsed mode
4. Maximum gate pulse width in pulsed mode (internally limited)
5. Measured at max. pulse width at nominal P1dB level
6. Rising edge measured from rising edge of GATE pulse to 90% RF output voltage.
Falling edge measured from falling edge of GATE pulse to 10% RF output voltage
7. Self resetting protection shuts the amplifier off if the load SWR is excessive
8. Pin out at www.tomcof.com/pdf/interface.pdf
9. Other connector types available on request
10. 3-pin IEC. Mains supply must include an earth

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Typical peak envelope power plots



Harmonics

